AICTE MANDATORY DISCLOSURES

I. A) NAME OF THE SOCIETY

ENGINEERING COLLEGE (KARIKAL) SOCIETY
(Registration No. 780/2007)
Registered under Societies Registration Act 1860

B) NAME OF THE INSTITUTION

PERUNTHALAIVAR KAMARAJAR INSTITUTE OF ENGINEERING AND TECHNOLOGY (PKIET)
(Government of Puducherry Institution)
NEDUNGADU Post,   KARAIKAL – 609 603
Telefax : (04368) 262234, Tel : 262011, 262256, 2622566
Website : www.pkiet.edu.in

II. NAME & ADDRESS OF THE PRINCIPAL

Dr. P. THAMBIDURAI, M.E., Ph.D., FIE., LMCSI., LMISTE
Professor of CSE & Principal
PERUNTHALAIVAR KAMARAJAR INSTITUTE OF ENGINEERING AND TECHNOLOGY (PKIET)
(Government of Puducherry Institution)
NEDUNGADU Post, KARAIKAL – 609 603
Telefax : (04368) 262234, Tel : 262011, 262256, 2622566
Website : www.pkiet.edu.in

III. NAME OF THE AFFILIATING UNIVERSITY

PONDICHERRY UNIVERSITY
PUDUCHERRY – 605 014.

IV.   GOVERNANCE

The college is managed by

(i)   The General Body;

(ii)   The Governing Body and the Executive Committee; and

(iii)   Such other committees as have been appointed under these present and other authorities as may be appointed by the General Body from time to time specifying their duties, powers and functions.

The Governing Body consists of the following persons, namely :-

S.No.	Name and Occupation of Members	Designation
1.	The Secretary to Government (Education), Government of Puducherry	Chairman
2.	The Secretary to Government (Finance), Government of Puducherry	Member
3.	The Collector-cum-District Magistrate, Karaikal District, Karaikal	Member
4.	Joint Secretary to Government (EDN-I), Government of Puducherry	Member
5.	The Director of Higher & Technical Education, Government of Puducherry	Member
6.	The Vice Chancellor's Nominee Pondicherry University, Puducherry	Member
7.	Regional Officer or his nominee, AICTE, Southern Region, Chennai	Member
8.	The Principal of the College	Member Secretary

V. PROGRAMMES

S.No.	Name of the Programme	Duration	Sanctioned Intake
1.	B.Tech. (Computer Science & Engineering)	4 years	60
2.	B.Tech. (Information Technology)	4 years	60
3.	B.Tech. (Electronics and Communication & Engineering)	4 years	60

VI. FACULTY

The college has been established during the academic year 2007-08 and Posts have been created in the cadre ratio of 1:2:6 as per AICTE norms. At present, the faculty strength is as follows :

S.No.	Discipline	Professor	Associate Professor	Assistant Professor (Stage-1)	Assistant Professor (Stage-2)	Assistant Professor (Stage-3)	Status
1.	CSE	1	1	6	-	-	Permanent
2.	IT	-	1	5	1		Permanent
3.	ECE	-	-	5	1		Permanent
4.	Civil	-	-	1	1	1	Permanent
5.	Mechanical	-	-	1	1		Permanent
6.	Maths	-	-	1	1		Permanent
7.	Physics	-	-	-	1	1	Permanent
8.	Chemistry	-	-	-	2	-	Permanent
9.	English	-	-	-	2	-	Permanent
10.	Librarian			-	1	-	Permanent

VII. PROFILE OF PRINCIPAL WITH QUALIFICATIONS, TOTAL EXPERIENCE, AGE AND DURATION OF EMPLOYMENT AT THE INSTITUTE CONCERNED

PRINCIPAL

Dr. P. THAMBIDURAI, M.E., Ph.D., F.I.E., LMCSI., LMISTE

VIII. FEES

The fee structure for the B.Tech. programmes is as follows :

S.No.	Description	First Year	Second Year	Third Year	Fourth Year
1.	Admission fee	1000	-	-	-
2.	Special fee per annum	3000	3000	3000	3000
3.	Library fee per annum	1500	1500	1500	1500
4.	Library deposit	1000	-	-	-
5.	Computer Charges per annum	1500	1500	1500	1500
6.	Student council fee	200	-	-	-
7.	Student welfare fund fee	200	200	200	200
8.	Cultural fee per annum	500	500	500	500
9.	Laboratory fee per annum	1000	1000	1000	1000
10.	Internet fee	500	500	500	500
11.	Alumni life member fee	200	-	-	-
12.	Caution deposit	5000	-	-	-
13.	University development fee	1000	-	-	-
14.	Recognition fee	180	-	-	-
15.	Matriculation fee	20	-	-	-
16.	Registration fee	40	-	-	-
17.	Sports fee	25	-	-	-
18.	Syllabus	50	-	-	-
19.	Tuition fee	15000	15000	15000	15000
	TOTAL	**31915**	**23200**	**23200**	**23200**

IX. ADMISSION

Admission to First year of the B.Tech. course has been centralized and conducted by the **CENT**ralised **A**dmission **C**ommittee (CENTAC), Government of Puducherry. Details of the admission will be published in leading newspapers by the CENTAC during the month of April / May every year.

X. ADMISSION PROCEDURE

A) Minimum Qualification for Admission:

Candidates seeking admission to the B.Tech. degree course should have passed the H.Sc (Academic) examination conducted by the Board of Higher Secondary Examination of Tamilnadu, or any other equivalent examination thereto, with a minimum of 45% marks (40% marks for OBC/BCM/MBC/EBC/BT/SC/ST candidates) in aggregate, in the prescribed subjects, namely: Mathematics, Physics and any one of the following optional subjects: Chemistry/Bio-Technology/ Computer Science/Biology (Botany & Zoology).

Candidates belonging to UT of Puducherry, and who have passed H.Sc. (Vocational Stream in the area of Engineering & Technology) examination, conducted by the Board of Higher Secondary examination of Tamil Nadu or any other equivalent examination thereto with a minimum of 45% marks (40% marks for OBC/BCM/MBC/EBC/BT/SC candidates) in aggregate, in the prescribed subjects, namely: Vocational subjects (Theory and Practical) and a related subject (Mathematics / Physics / Chemistry), are also eligible for admission to the B.Tech degree course.

B) Admission to Third Semester / Second Year (Lateral Entry):

A pass in the three years Diploma Course or four years sandwich Diploma Course in Engineering / Technology with a minimum of 45% marks (40% for OBC/BCM/MBC/EBC/BT/SC candidates) in aggregate in the subjects covered in 3rd to 6th Semesters.

OR

A pass in any B.Sc Degree (with Mathematics as one of the subjects at Degree Level) with a minimum of 45% marks (40% for OBC/BCM/MBC/EBC/BT/SC candidates) in aggregate in subjects covered from first to last semesters except language subjects.

XI. CRITERIA AND WEIGHT AGES FOR ADMISSION

a) Selection of Candidates for admission to all the Degree courses will be based on the Merit lists prepared by CENTAC, based on the marks arrived at by adopting the 'method of normalization'. Ranking of the candidates will be done based on the 'normalized marks' (rounded-off to three decimal places) arrived at, to a base of 200.

b) The selection of the candidate shall only be based on the marks obtained in the **PRE-FINAL AND FINAL SEMESTER EXAM** of the candidates in the Diploma/B.Sc. Degree course. During the preparation of merit list if a tie occurs, the procedure given below will be followed one after the other to break the tie. The aggregate in percentage of the subjects of qualifying semesters of Diploma/Degree examination shall be the basis for merit. In the event of this also being the same, the total percentage of marks obtained in S.S.L.C. / H.Sc. or equivalent examination shall be the basis for ascertaining the merit / Date of birth of higher age.

XII. APPLICATION FORM

Refer : www.centacprof.net.in

XIV. RESULTS OF ADMISSION UNDER MANAGEMENT SEATS / VACANT SEATS

There is no management quota seats. The college is promoted by the U T Government of Puducherry.

XV. INFORMATION ON INFRASTRUCTURE AND OTHER RESOURCES AVAILABLE

The College has good infrastructure and state-of-art facilities. For details please refer our website www.pkiet.edu.in.

XVI. LIBRARY

The College Library caters to the needs of learning and teaching styles through the provision of access to a wide range of reading materials both in hard and digital form. It facilitates the planning and implementation of effective teaching-learning process so that it will equip students with the skills necessary to succeed in a constantly changing social and economic environment. The library has subscribed ejournals to access both International and National level through AICTE portals. The functions of the Library are fully automated through AUTOLIB software. Digital library will be created with back volumes in digital form.

XVII. LABORATORY

a) Computer Science and Engineering

The following labs have been established in this department.
1. Computer Centre
2. Wireless Lab
3. Hardware and Trouble Shooting Lab
4. Digital Lab
5. Microprocessor and Micro Controller Lab

b) Information and Technology

The following labs have been established in this department.

1. Software Engineering Lab
2. Thin Client Lab
3. Web Technology Lab

c) Electronics and Communication Engineering

The following labs have been established in this department.

1. Basic Electrical and Electronics Lab
2. Electrical Machines Lab
3. Electronic Devices and Circuits Lab
4. Communication Lab
5. Electronic System Design Lab
6. Advanced Communication Lab
7. Design and Simulation Lab

d) Language Laboratory

A well-equipped Language Lab with 30 Nos. of Intel Core 2 Duo Processors systems and a set of Globarena Language Development Software which includes Group Discussion, Interview Skills, Personality Development, GMAT, GRE, IELTS has been set up to hone students facing competitive examinations such as IELTS, TOEFL, GRE, GMAT, CAT etc. The weak students in Language and Communication have been identified and provide special coaching in Oral and Written forms of the Language and train them in effective Presentation Skills.

XVIII. COMPUTING FACILITIES

1. Around 300 computer systems with latest configurations have been purchased and installed in different labs. and students make use of them.

2. A 2 Mbps Unlimited Broadband connectivity from BSNL under the plan Business UL 15000 Plus has been obtained and installed in the computer centre for the benefit of the staff and students. Wi-Fi connectivity is also provided within the campus for the staff and students.

3. The Institute has implemented National Mission on Education through Information and Communication Technology (NMEICT) promoted by the Ministry of Human Resource Development (HRD) in collaboration with various IITs including IIT Bombay and PSU Major BSNL, India. This project facilitates to install 24 x 7 Virtual Private Network (VPN) of 10 Mbps (20 nodes of 512 Kbps each) under National Mission on Education through Information and Communication Technology (NMEICT) Project promoted by MHRD, New Delhi and implemented through BSNL, Kumbakonam. Internet access is available through BSNL-MPLS Cloud with Unlimited data access.

XIX. WORKSHOP

Basic Engineering concepts and Workshop practice are the essential components of Engineering education. Exposure through practice is an excellent way to learn about the ease-of-use of Welding, Fitting and Carpentry. A spacious Drawing hall is available to practice Engineering Graphics. This will help the students to understand the Design and Implementation concepts. Material Testing is being undertaken and Material Testing Lab with state-of-art facilities will be setup.

XX. TEACHING LEARNING PROCESS:

Primarily chalk and talk method is used for teaching. To improve the effective teaching, Teaching aids such as OHP, Multimedia Projector are being used by the faculty members. The students are divided into groups of 20 each and a teacher is assigned to each group. The students are tutored by the respective group teachers to improve their skills. Regular assignments and tests are being conducted and the performance of the students is evaluated. The poorly performing students have been identified and special coaching is given to them.